

Figure 1

FIGURE 2

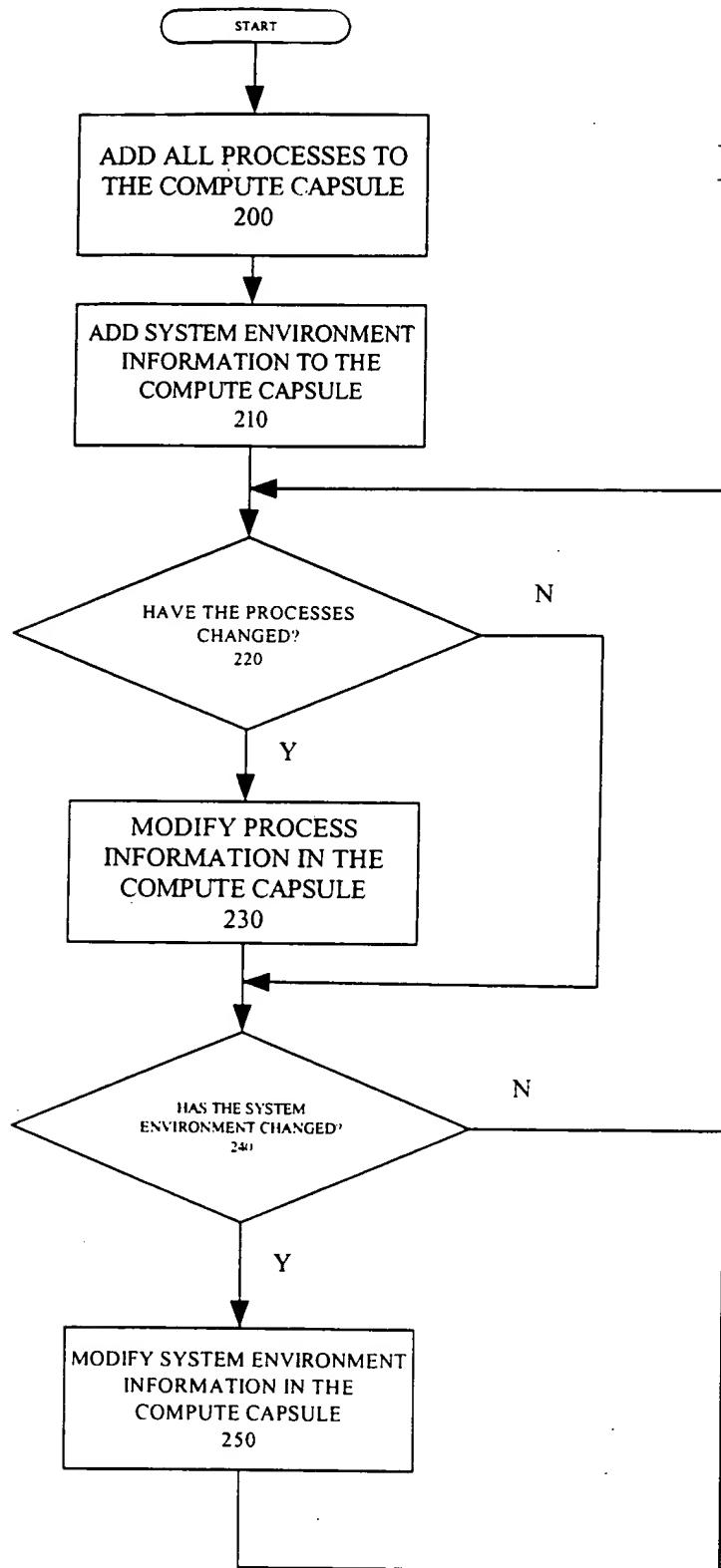


FIGURE 3A

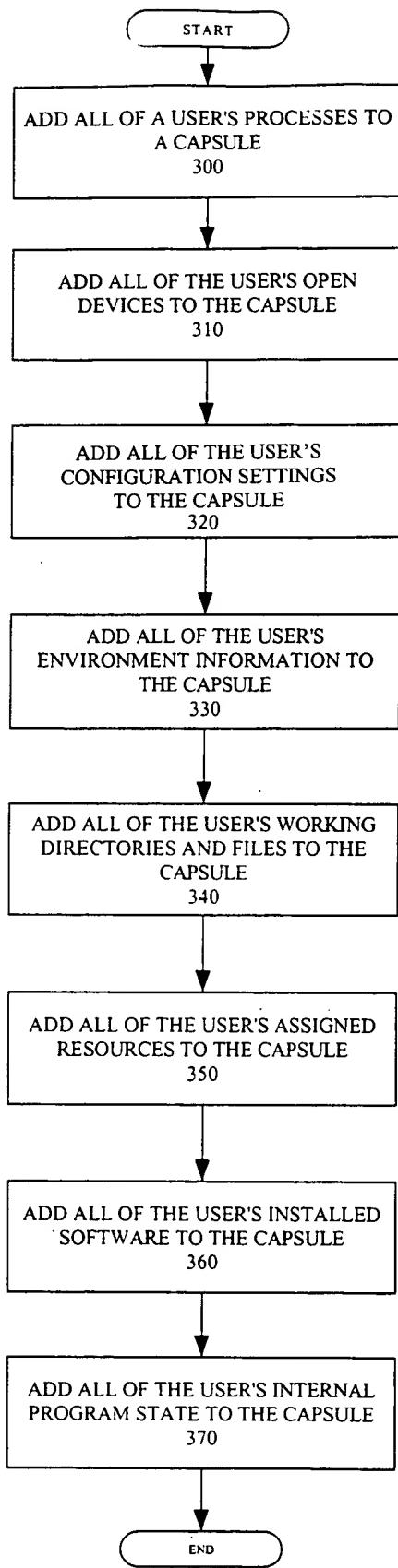


FIGURE 3B

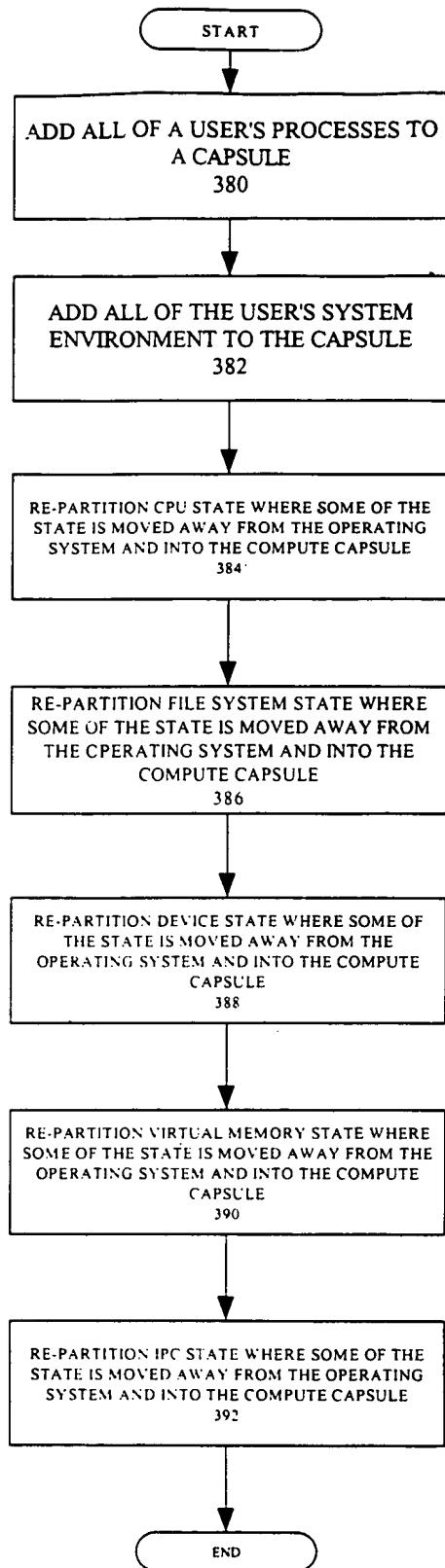
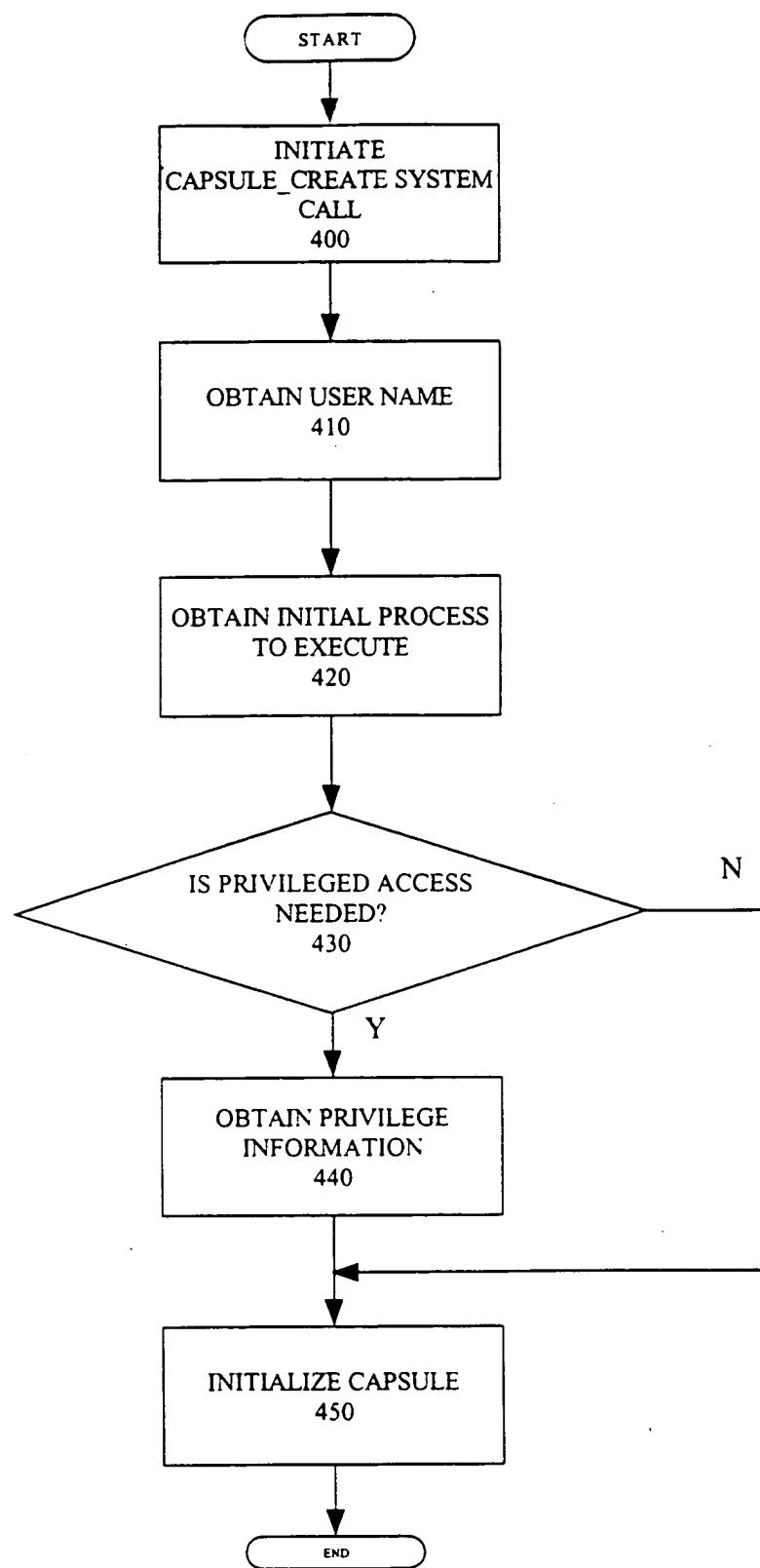


FIGURE 4



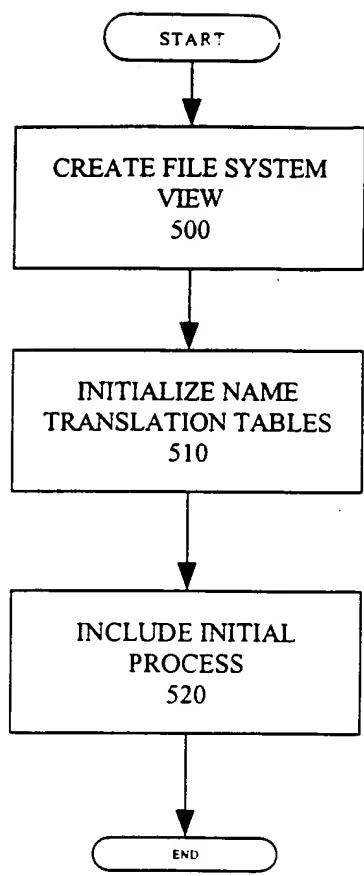


FIGURE 5

FIGURE 6

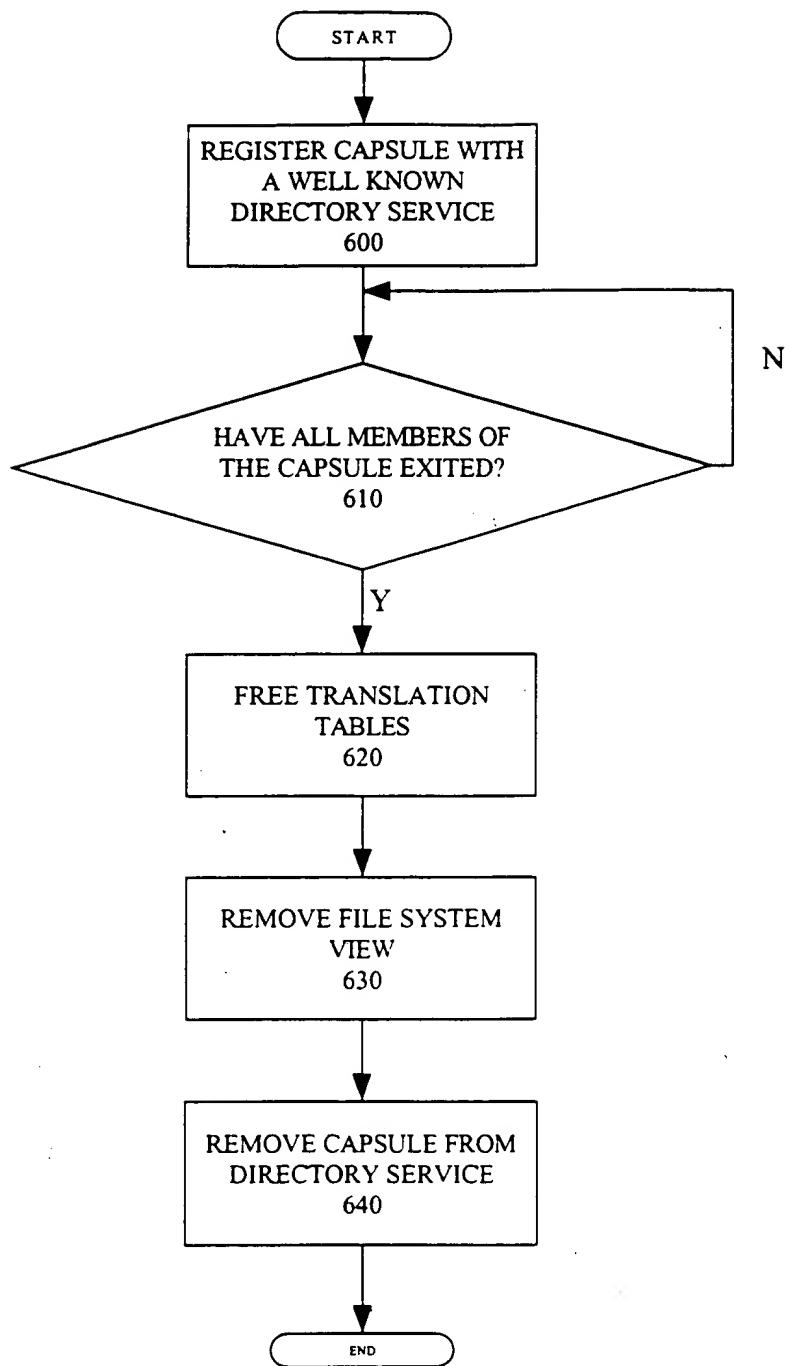


FIGURE 7

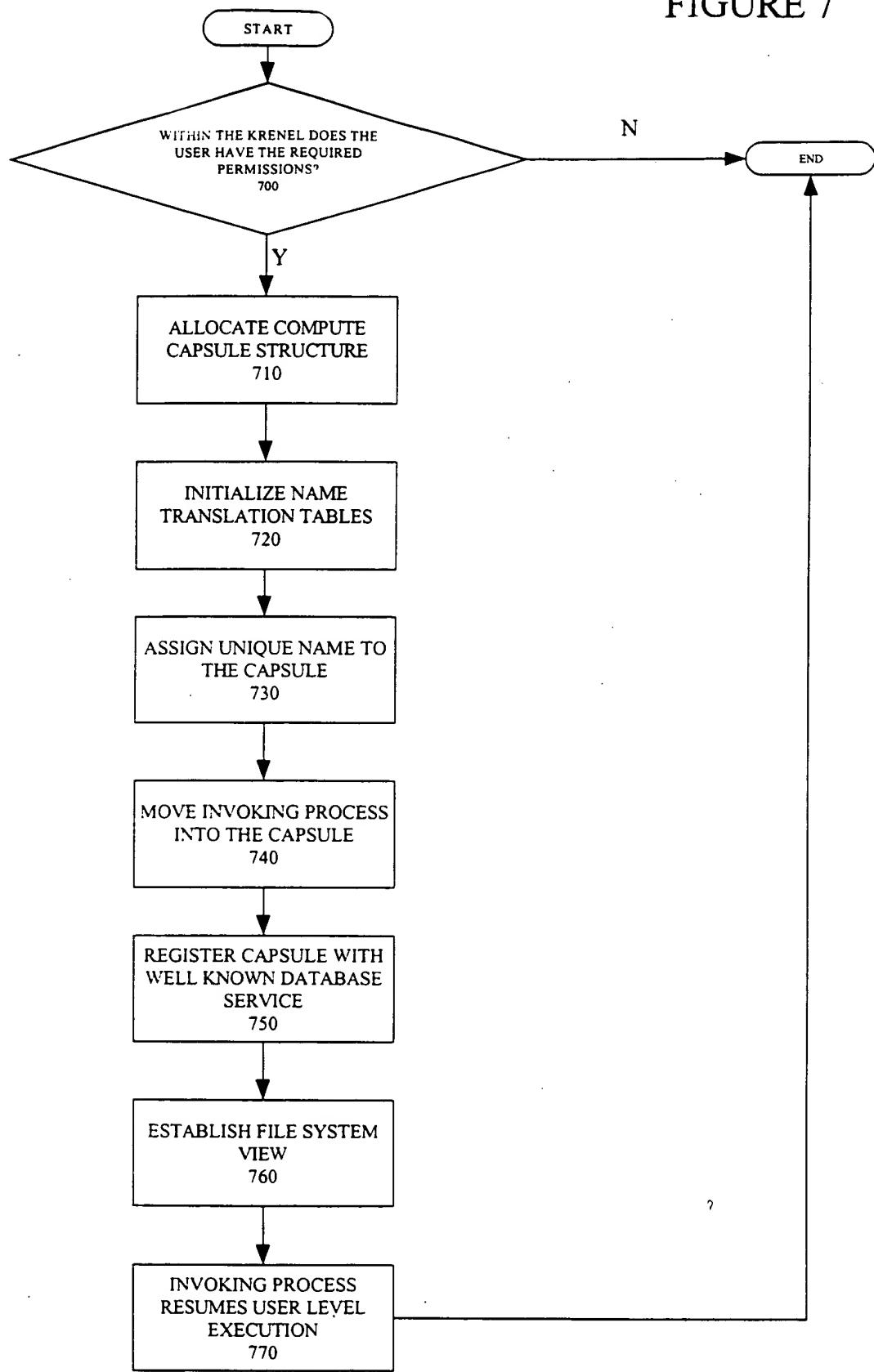


FIGURE 8

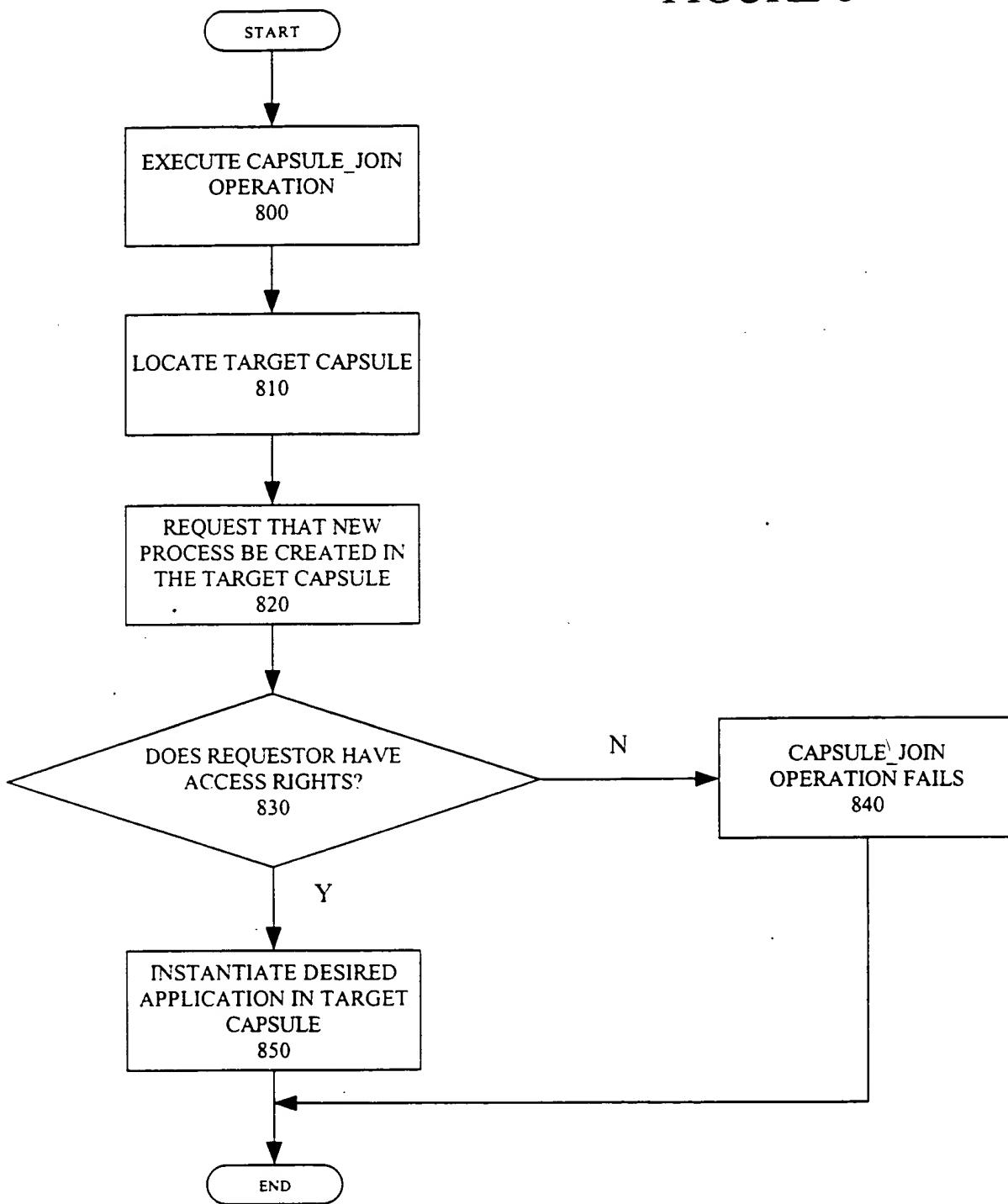
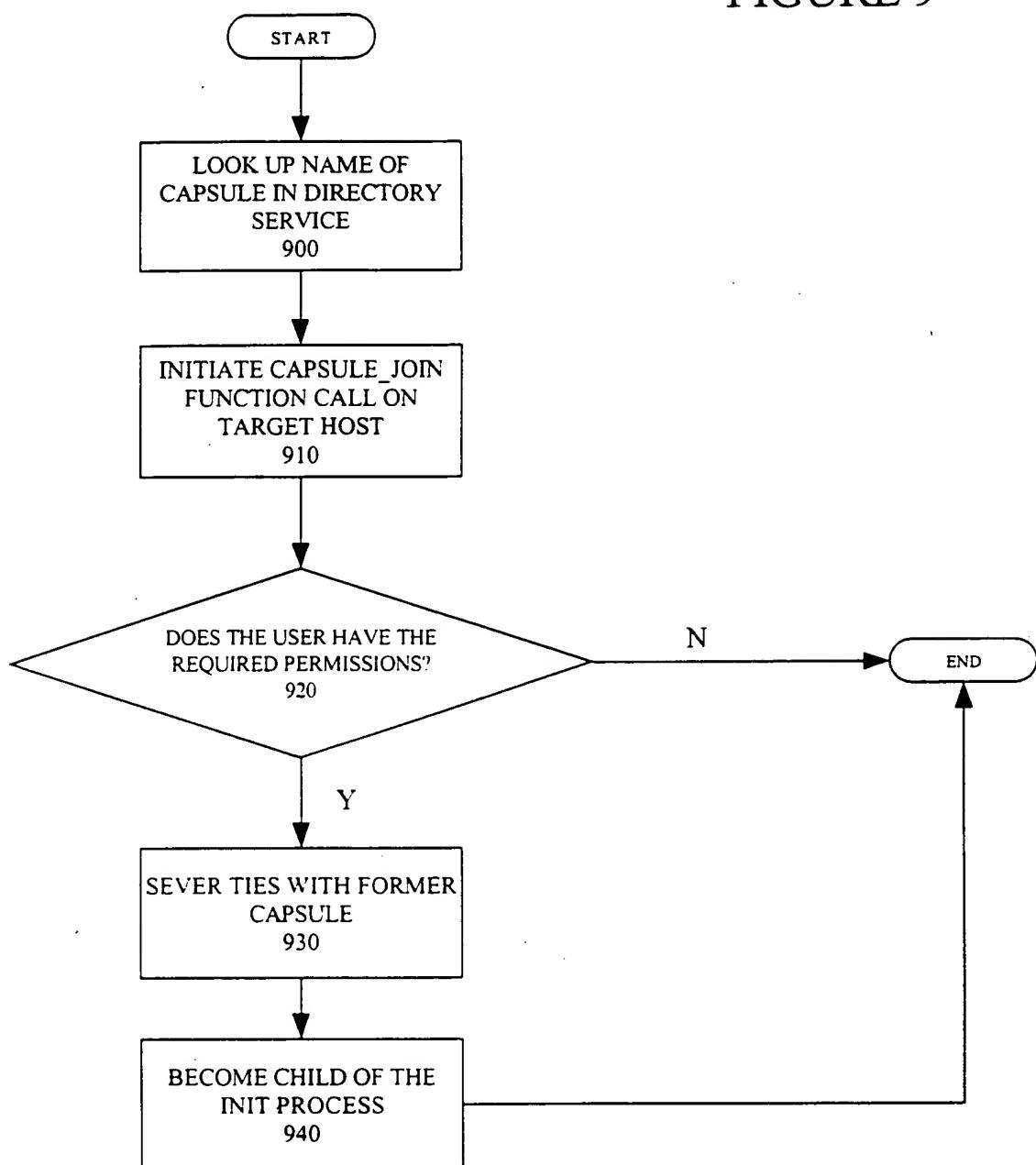


FIGURE 9



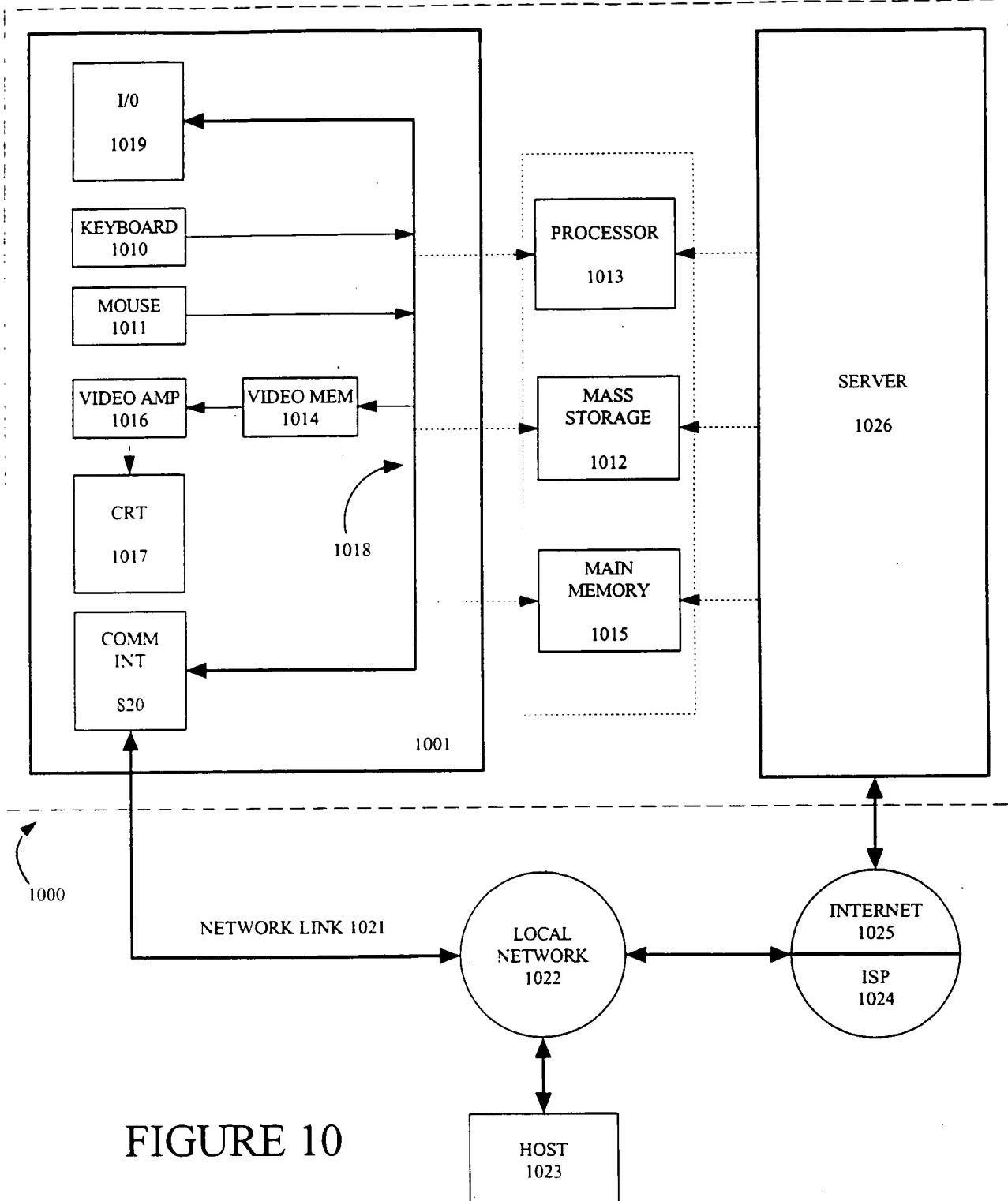
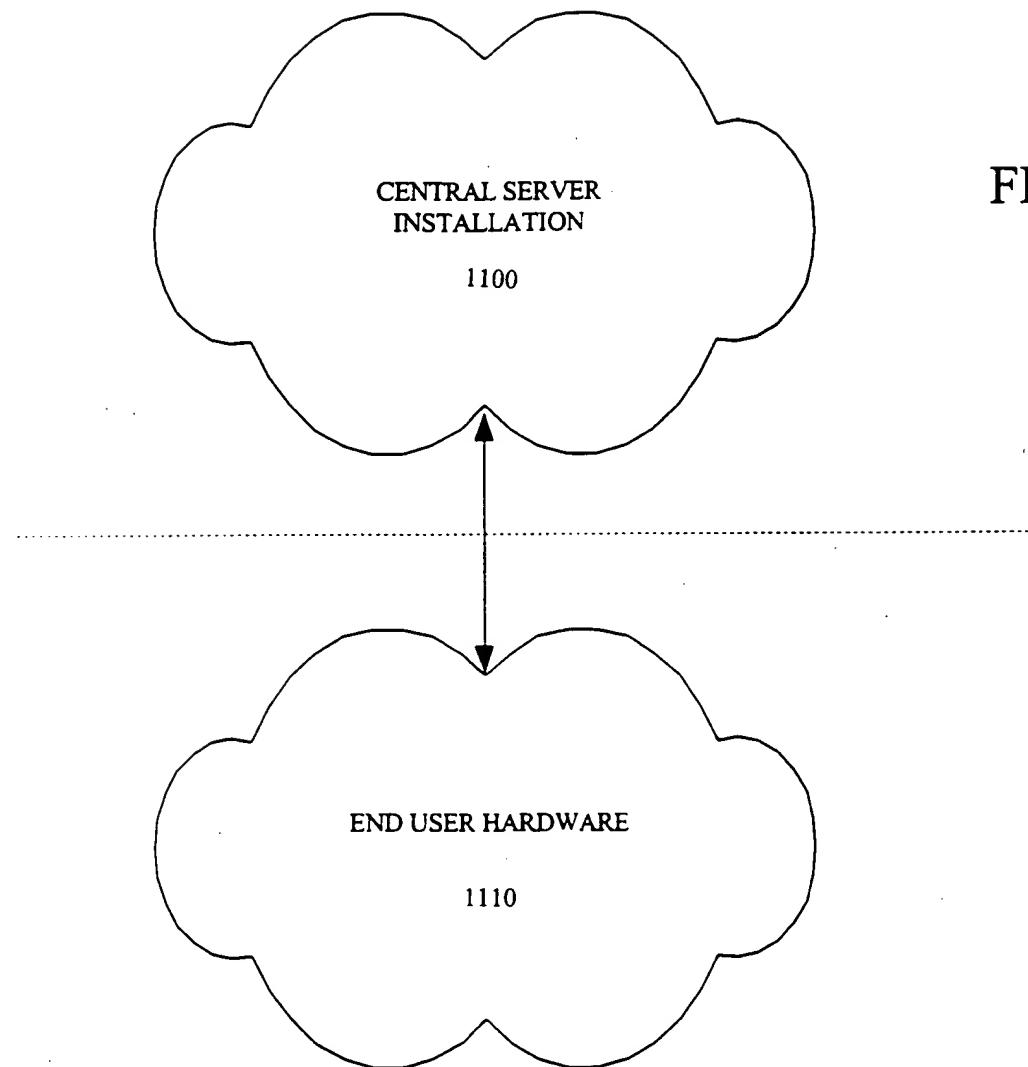
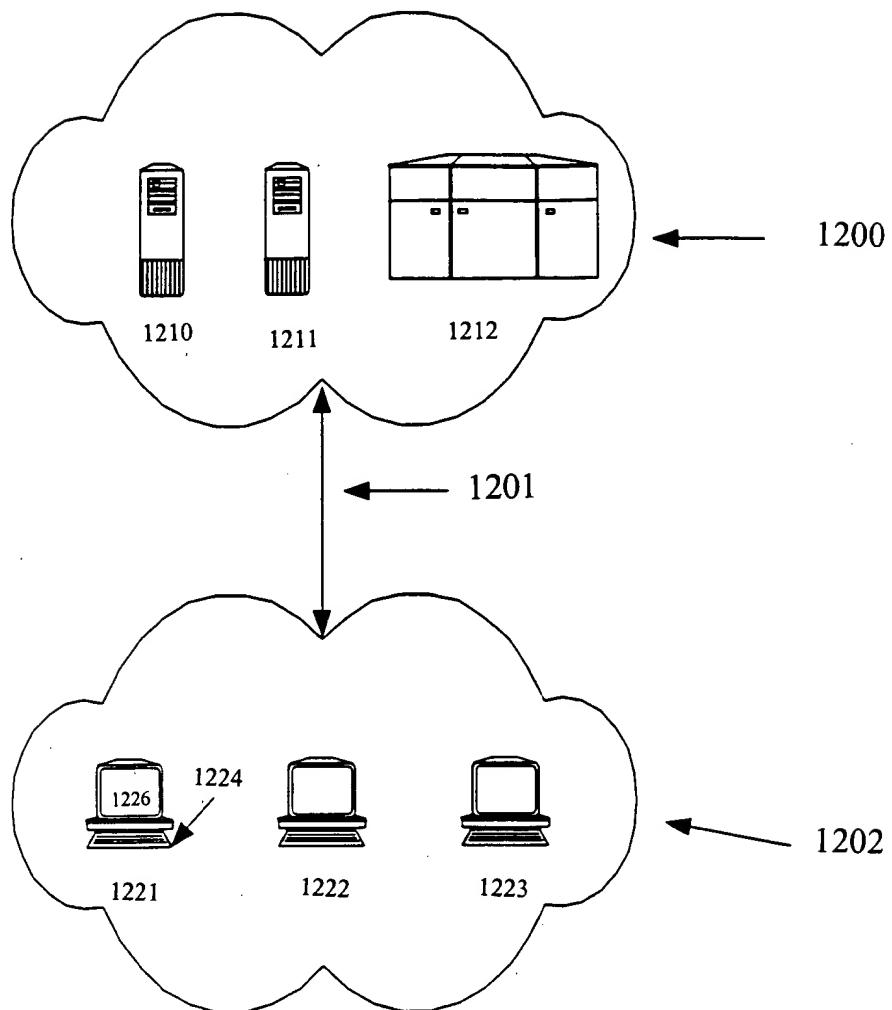


FIGURE 10

**FIGURE 11**





## FIGURE 12

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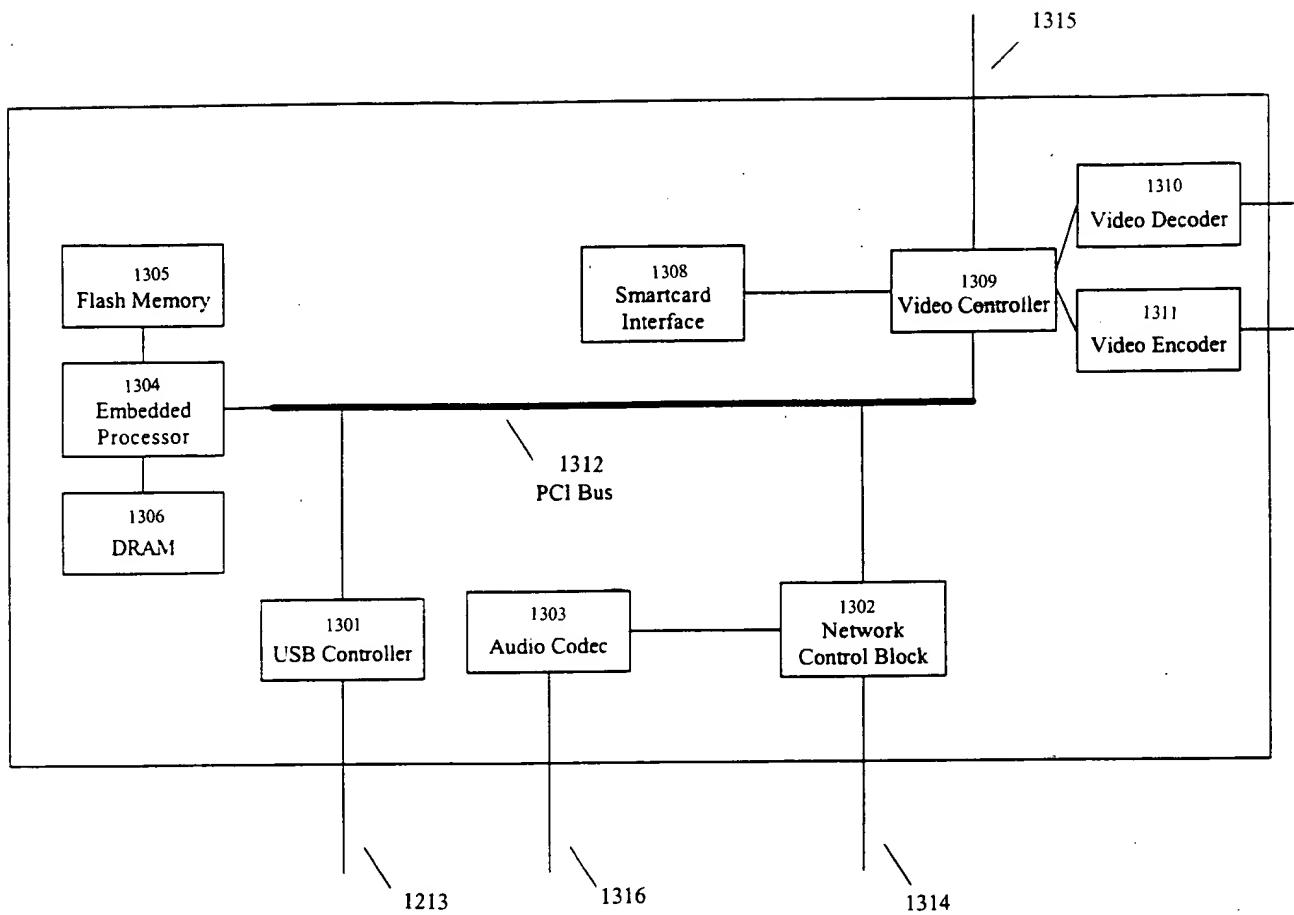
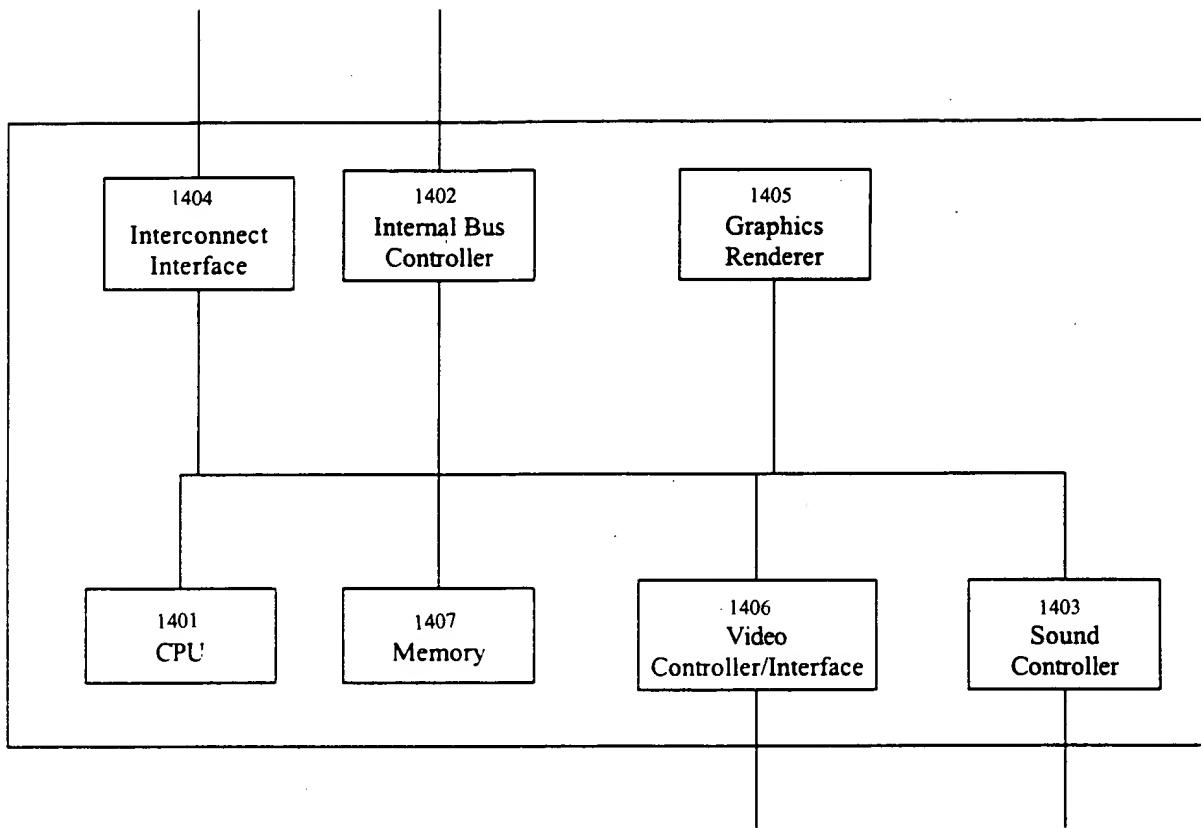
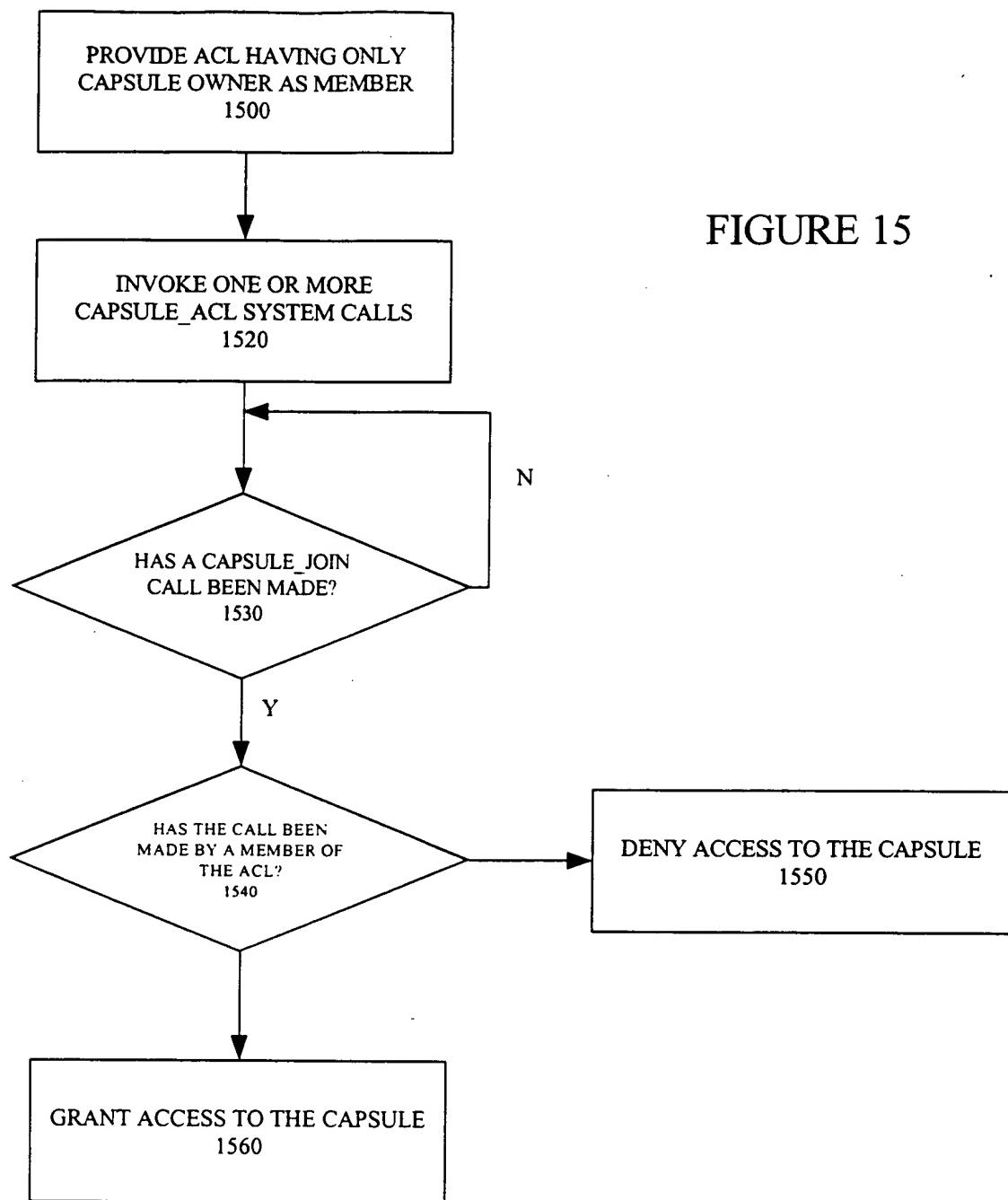


Figure13: HID



**Figure 14:** HID Single Chip Implementation

FIGURE 15



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